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**Tseng**

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(54) **PUSH-PULL DRIVER WITH STAGE  
INVERSION AND METHOD OF OPERATION**

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21, 2012.

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**H02J 17/00** (2006.01)  
**H02J 7/02** (2006.01)  
**H04B 5/00** (2006.01)

(52) **U.S. Cl.**  
CPC ..... **H01F 38/14** (2013.01); **H02J 7/025**  
(2013.01); **H02J 17/00** (2013.01); **H04B**  
**5/0037** (2013.01); **H04B 5/0075** (2013.01)

(58) **Field of Classification Search**  
CPC ..... H01F 38/14; H02J 17/00  
See application file for complete search history.

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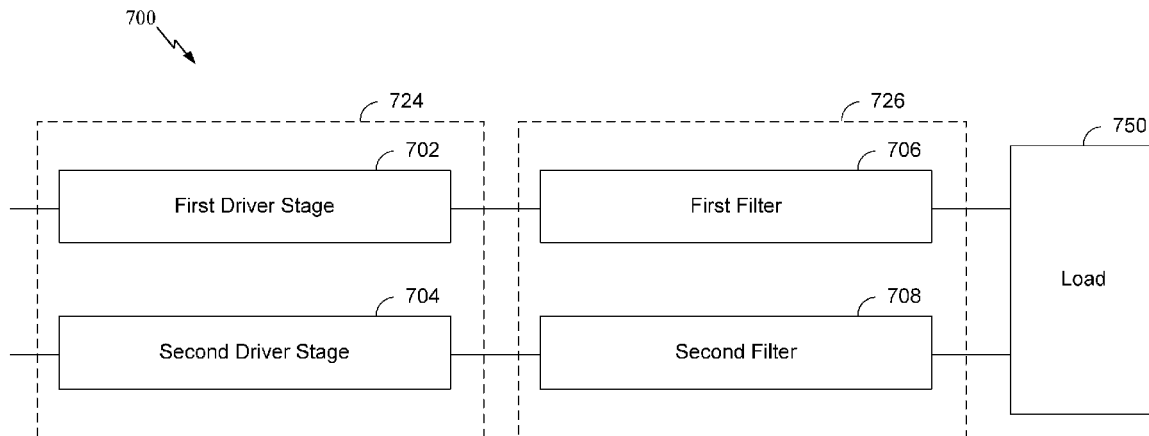
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(57) **ABSTRACT**

Systems, methods, and apparatus for amplifying a voltage in  
wireless power transmitters are disclosed. In one aspect, the  
transmitter comprises a first circuit configured to generate a  
first signal. The first signal comprises a non-inverted output.  
The transmitter further comprises a second circuit configured  
to generate a second signal in phase with the first signal. The  
second signal comprises an inverted output with respect to the  
non-inverted output. The first circuit and the second circuit  
are further configured to drive a load.

**35 Claims, 8 Drawing Sheets**



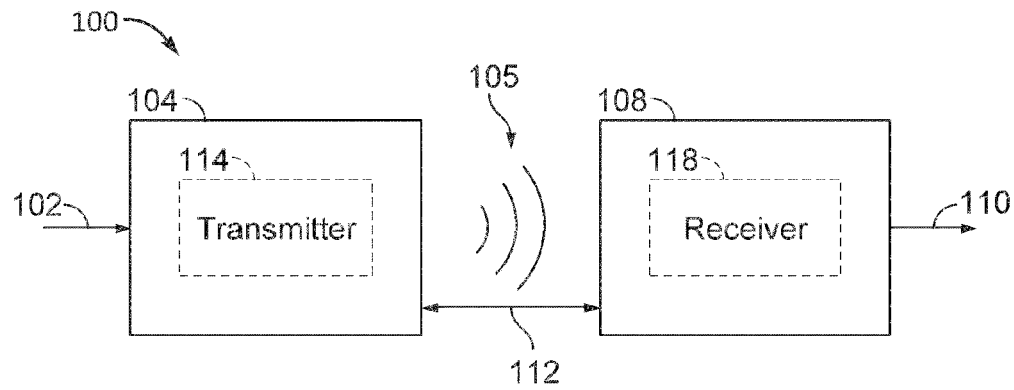


FIG. 1

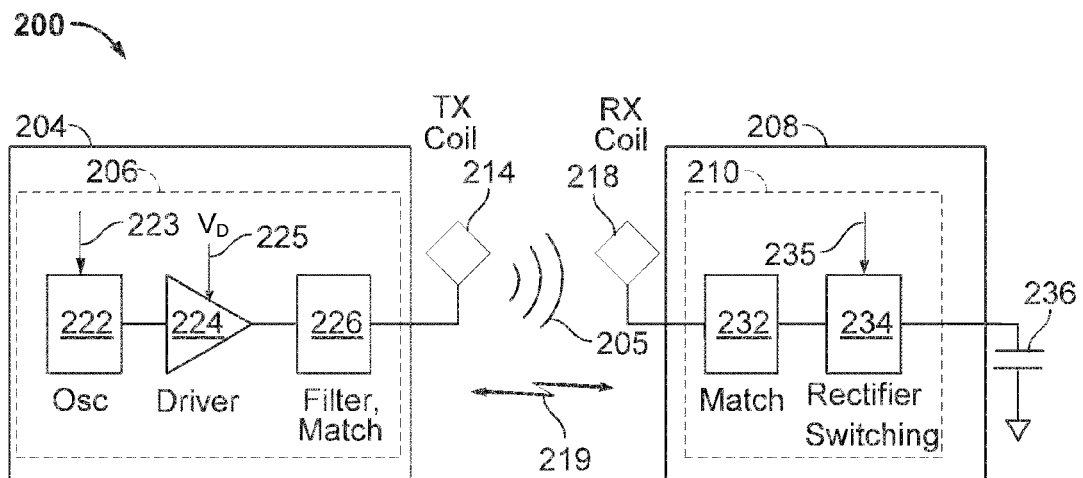


FIG. 2

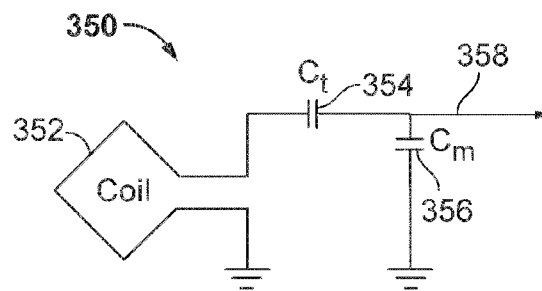


FIG. 3

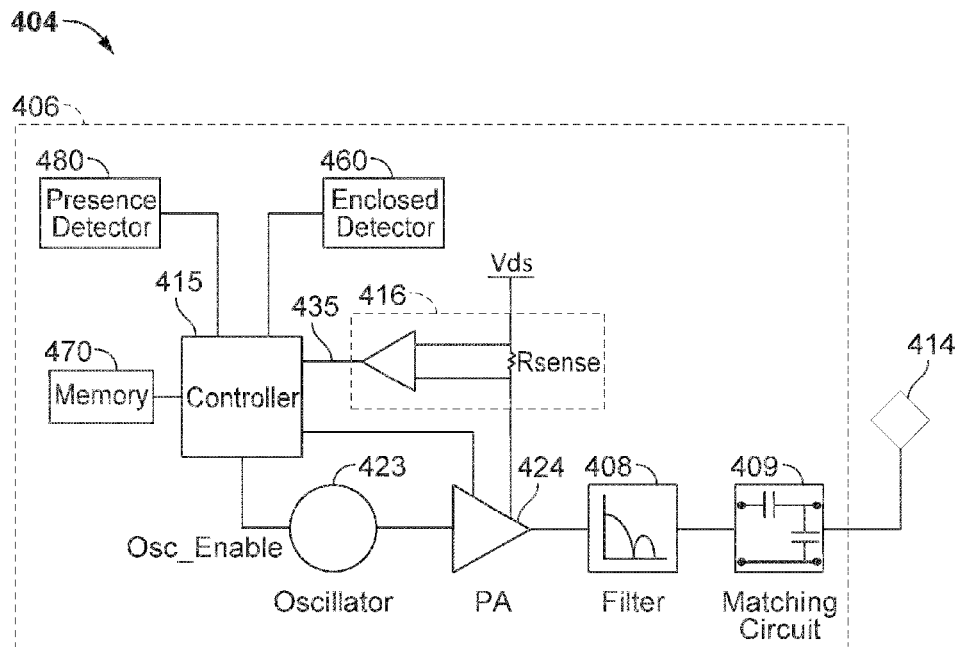


FIG. 4

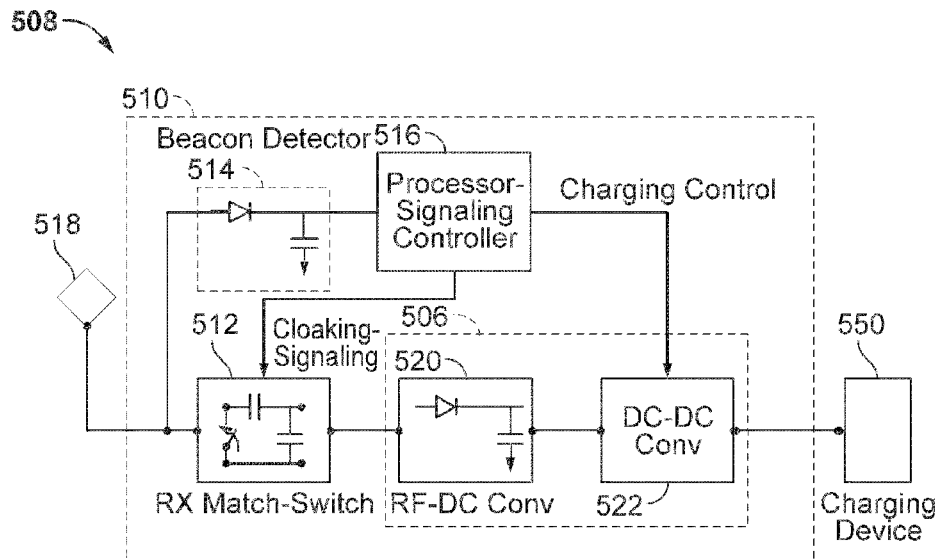


FIG. 5

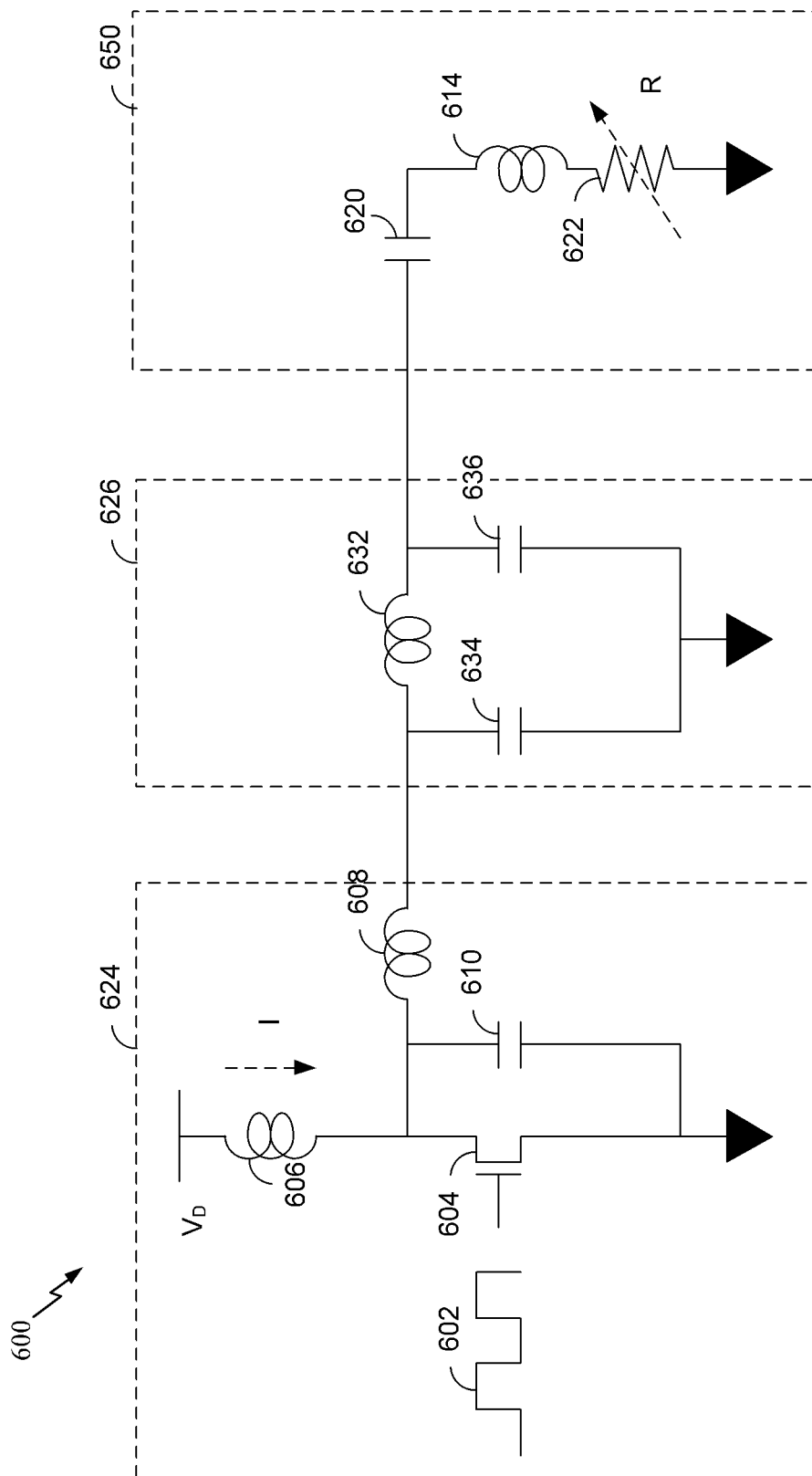
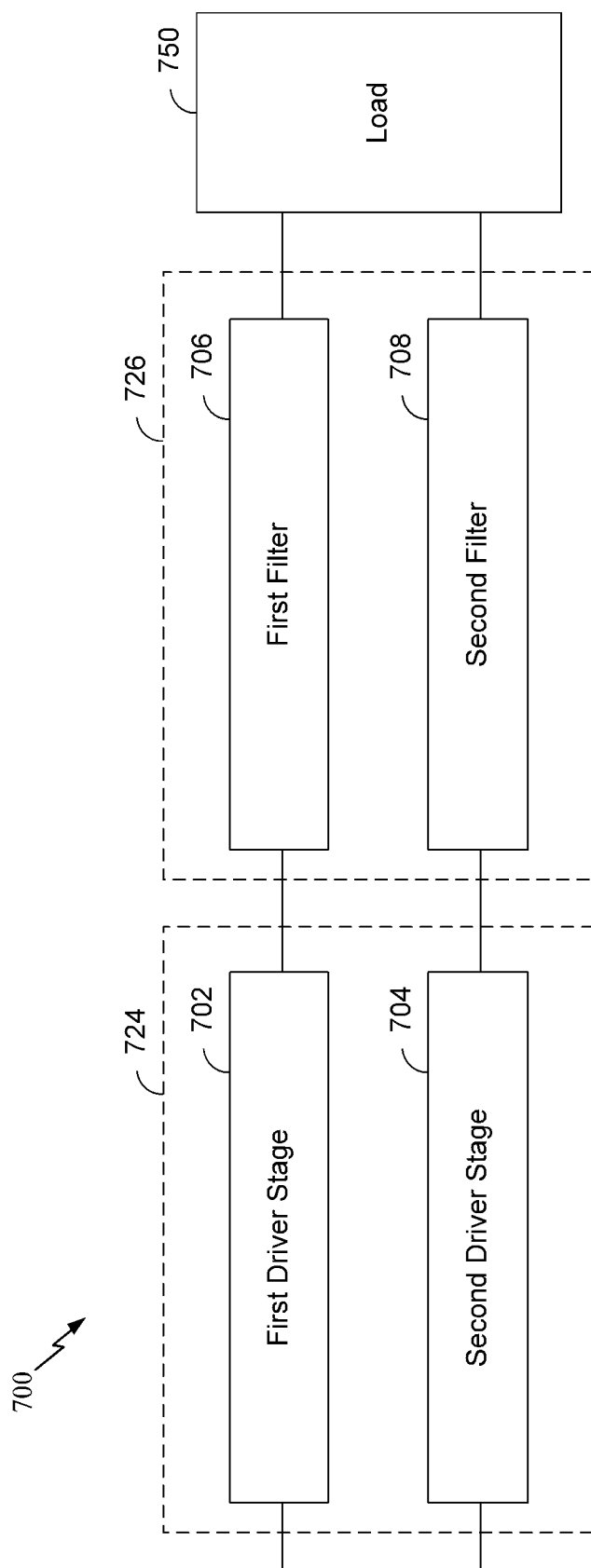


FIG. 6



**FIG. 7**

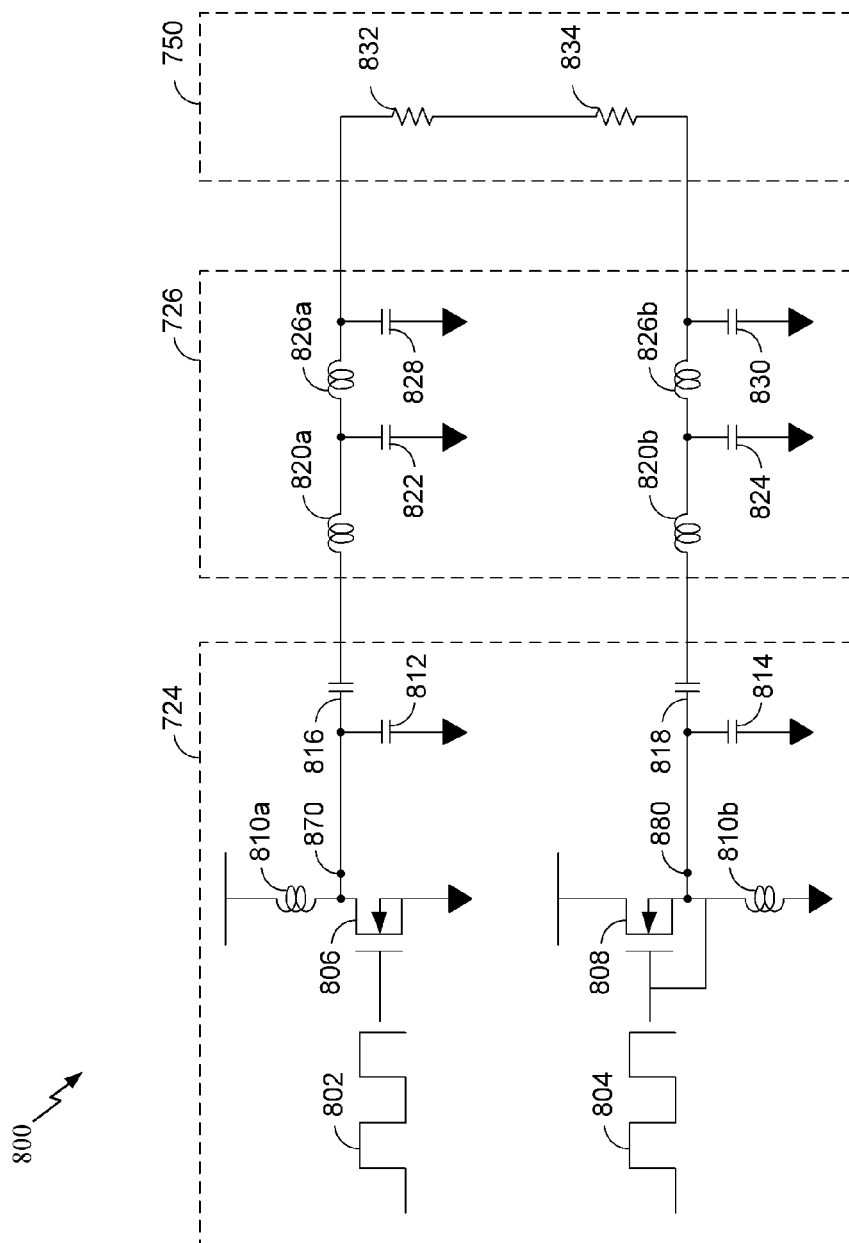


FIG. 8A

800 ↗

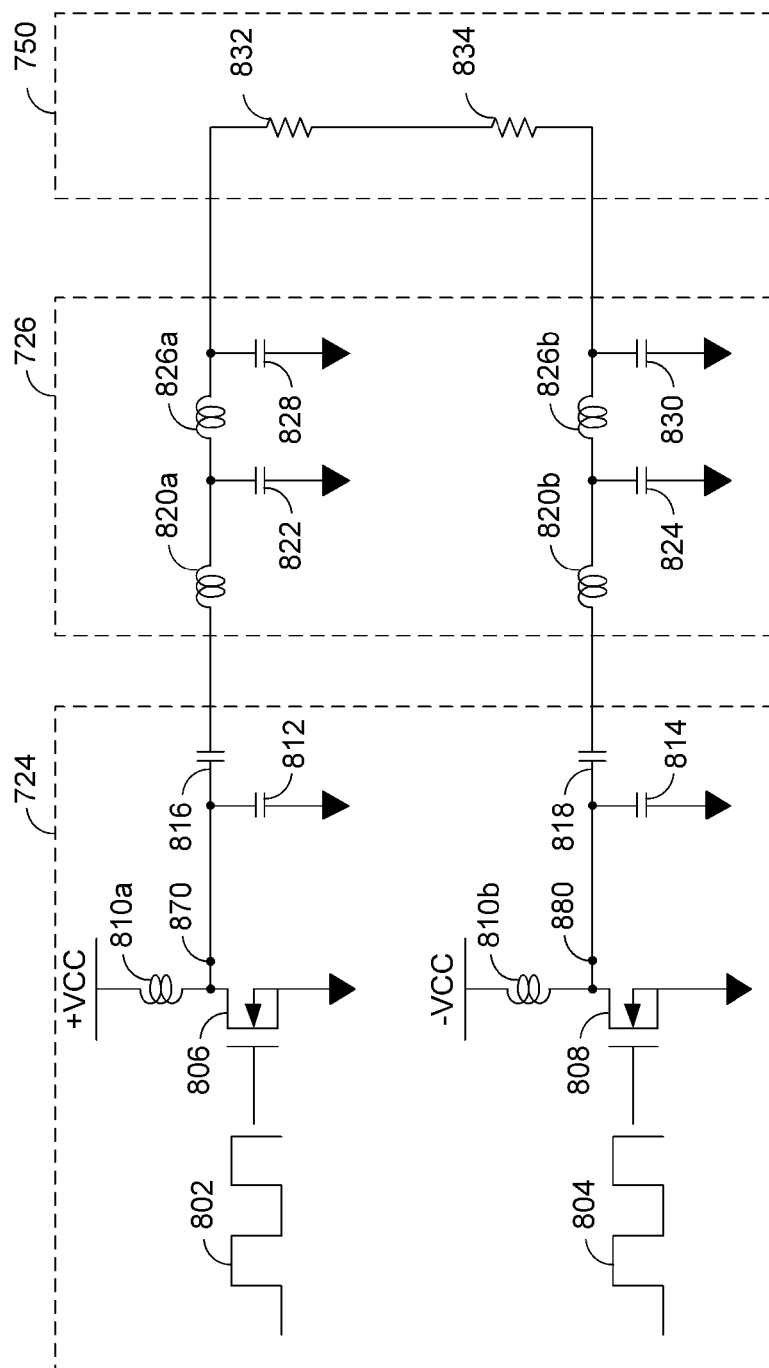


FIG. 8B

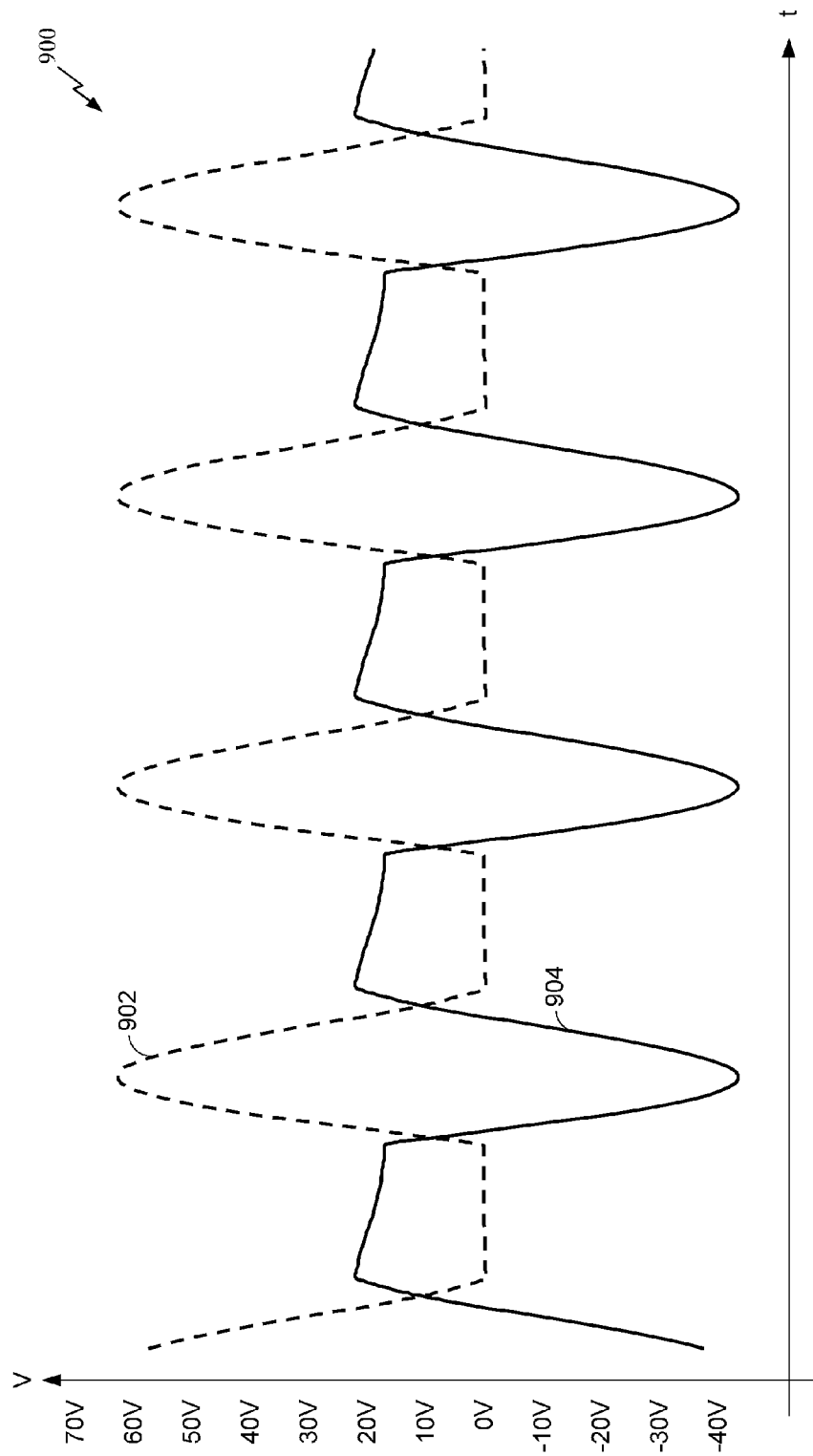
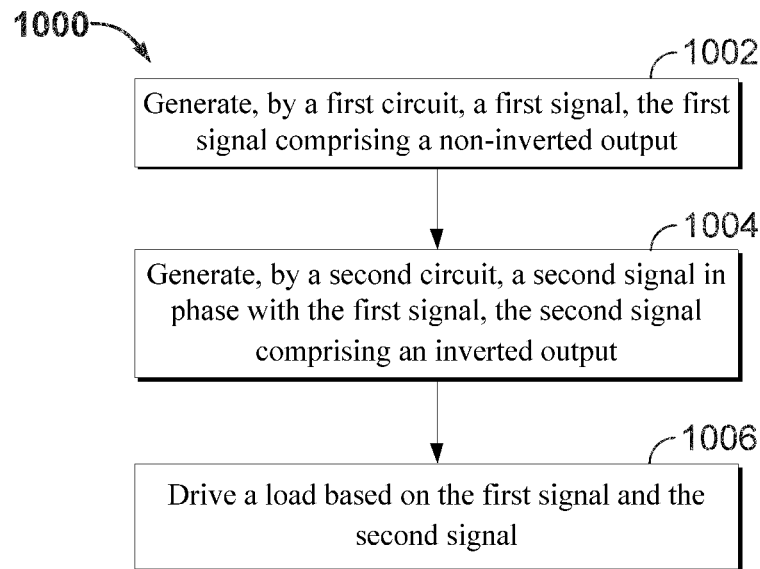
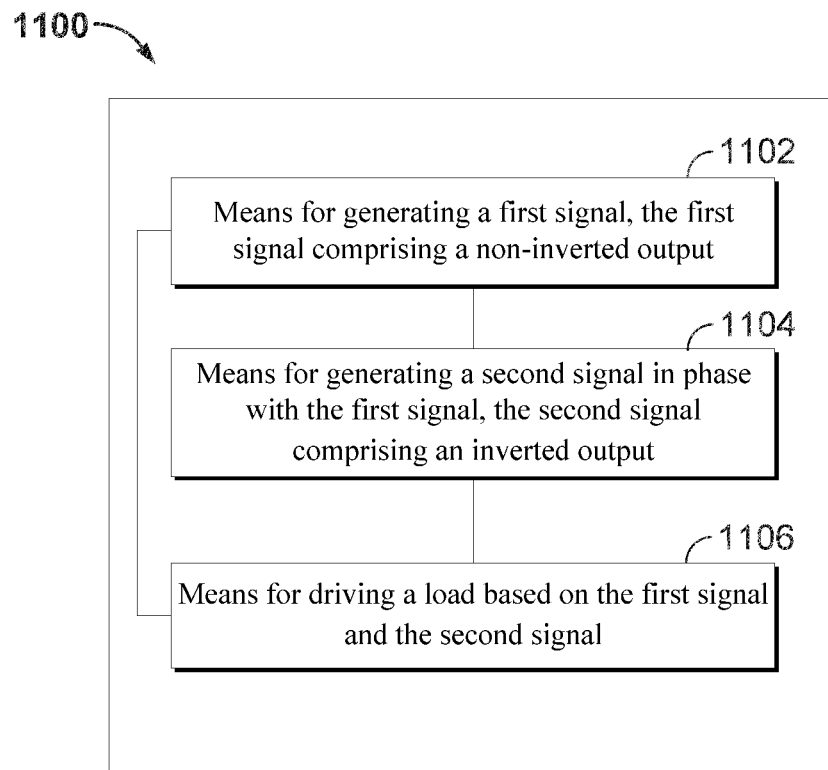


FIG. 9



**FIG. 10****FIG. 11**

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## PUSH-PULL DRIVER WITH STAGE INVERSION AND METHOD OF OPERATION

### CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims priority under 35 U.S.C. §119(e) to U.S. Provisional Patent Application No. 61/601,469, entitled “PUSH-PULL DRIVER WITH STAGE INVERSION” and filed on Feb. 21, 2012, the entire contents of which disclosure is herewith incorporated by reference.

### FIELD

The present invention relates generally to wireless power. More specifically, the disclosure is directed to amplifying a voltage in wireless power transmitters.

### BACKGROUND

An increasing number and variety of electronic devices are powered via rechargeable batteries. Such devices include mobile phones, portable music players, laptop computers, tablet computers, computer peripheral devices, communication devices (e.g., Bluetooth devices), digital cameras, hearing aids, and the like. While battery technology has improved, battery-powered electronic devices increasingly require and consume greater amounts of power, thereby often requiring recharging. Rechargeable devices are often charged via wired connections through cables or other similar connectors that are physically connected to a power supply. Cables and similar connectors may sometimes be inconvenient or cumbersome and have other drawbacks. Wireless charging systems that are capable of transferring power in free space to be used to charge rechargeable electronic devices or provide power to electronic devices may overcome some of the deficiencies of wired charging solutions. As such, wireless power transfer systems and methods that efficiently and safely transfer power to electronic devices are desirable.

### SUMMARY

Various implementations of systems, methods and devices within the scope of the appended claims each have several aspects, no single one of which is solely responsible for the desirable attributes described herein. Without limiting the scope of the appended claims, some prominent features are described herein.

Details of one or more implementations of the subject matter described in this specification are set forth in the accompanying drawings and the description below. Other features, aspects, and advantages will become apparent from the description, the drawings, and the claims. Note that the relative dimensions of the following figures may not be drawn to scale.

One aspect of the disclosure provides a transmitter for transmitting power wirelessly. The transmitter comprises a first circuit configured to generate a first signal. The first signal comprises a non-inverted output. The transmitter further comprises a second circuit configured to generate a second signal in phase with the first signal. The second signal comprises an inverted output with respect to the non-inverted output. The first circuit and the second circuit are further configured to drive a load.

Another aspect of the disclosure provides a method for amplifying a voltage. The method comprises generating, by a first circuit, a first signal. The first signal comprises a non-

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inverted output. The method further comprises generating, by a second circuit, a second signal in phase with the first signal. The second signal comprises an inverted output with respect to the non-inverted output. The method further comprises driving a load based on the first signal and the second signal.

Another aspect of the disclosure provides an apparatus for amplifying a voltage. The apparatus comprises means for generating a first signal. The first signal comprises a non-inverted output. The apparatus further comprises means for generating a second signal in phase with the first signal. The second signal comprises an inverted output with respect to the non-inverted output. The apparatus further comprises means for driving a load based on the first signal and the second signal.

Another aspect of the disclosure provides a non-transitory computer-readable medium comprising code that, when executed, causes an apparatus to generate, by a first circuit, a first signal. The first signal comprises a non-inverted output. The medium further comprises code that, when executed, causes the apparatus to generate, by a second circuit, a second signal in phase with the first signal. The second signal comprises an inverted output with respect to the non-inverted output. The medium further comprises code that, when executed, causes the apparatus to drive a load based on the first signal and the second signal.

### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a functional block diagram of an exemplary wireless power transfer system, in accordance with exemplary embodiments.

FIG. 2 is a functional block diagram of exemplary components that may be used in the wireless power transfer system of FIG. 1, in accordance with various exemplary embodiments.

FIG. 3 is a schematic diagram of a portion of transmit circuitry or receive circuitry of FIG. 2 including a transmit or receive coil, in accordance with exemplary embodiments.

FIG. 4 is a functional block diagram of a transmitter that may be used in the wireless power transfer system of FIG. 1, in accordance with exemplary embodiments.

FIG. 5 is a functional block diagram of a receiver that may be used in the wireless power transfer system of FIG. 1, in accordance with exemplary embodiments.

FIG. 6 is a schematic diagram of a portion of transmit circuitry that may be used in the transmit circuitry of FIG. 4.

FIG. 7 is a functional block of a portion of a wireless power transmitter that may be used in the wireless power transfer system of FIG. 1, in accordance with exemplary embodiments.

FIGS. 8A-B are schematic diagrams of a driver and a filter that may be used in the wireless power transmitter of FIG. 7.

FIG. 9 is a waveform diagram that may be generated by the driver of FIGS. 8A-B.

FIG. 10 is a flowchart of an exemplary method for amplifying a voltage in a wireless power transmitter.

FIG. 11 is a functional block diagram of a driver, in accordance with an exemplary embodiment.

The various features illustrated in the drawings may not be drawn to scale. Accordingly, the dimensions of the various features may be arbitrarily expanded or reduced for clarity. In addition, some of the drawings may not depict all of the components of a given system, method or device. Finally, like reference numerals may be used to denote like features throughout the specification and figures.

### DETAILED DESCRIPTION

The detailed description set forth below in connection with the appended drawings is intended as a description of exem-

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plary embodiments of the invention and is not intended to represent the only embodiments in which the invention may be practiced. The term “exemplary” used throughout this description means “serving as an example, instance, or illustration,” and should not necessarily be construed as preferred or advantageous over other exemplary embodiments. The detailed description includes specific details for the purpose of providing a thorough understanding of the exemplary embodiments of the invention. The exemplary embodiments of the invention may be practiced without these specific details. In some instances, well-known structures and devices are shown in block diagram form in order to avoid obscuring the novelty of the exemplary embodiments presented herein.

Wirelessly transferring power may refer to transferring any form of energy associated with electric fields, magnetic fields, electromagnetic fields, or otherwise from a transmitter to a receiver without the use of physical electrical conductors (e.g., power may be transferred through free space). The power output into a wireless field (e.g., a magnetic field) may be received, captured by, or coupled by a “receiving coil” to achieve power transfer.

FIG. 1 is a functional block diagram of an exemplary wireless power transfer system 100, in accordance with exemplary embodiments. Input power 102 may be provided to a transmitter 104 from a power source (not shown) for generating a field 105 for providing energy transfer. A receiver 108 may couple to the field 105 and generate output power 110 for storing or consumption by a device (not shown) coupled to the output power 110. Both the transmitter 104 and the receiver 108 are separated by a distance 112. In one exemplary embodiment, transmitter 104 and receiver 108 are configured according to a mutual resonant relationship. When the resonant frequency of receiver 108 and the resonant frequency of transmitter 104 are substantially the same or very close, transmission losses between the transmitter 104 and the receiver 108 are minimal. As such, wireless power transfer may be provided over larger distance in contrast to purely inductive solutions that may require large coils that require coils to be very close (e.g., mms). Resonant inductive coupling techniques may thus allow for improved efficiency and power transfer over various distances and with a variety of inductive coil configurations.

The receiver 108 may receive power when the receiver 108 is located in an energy field 105 produced by the transmitter 104. The field 105 corresponds to a region where energy output by the transmitter 104 may be captured by a receiver 108. In some cases, the field 105 may correspond to the “near-field” of the transmitter 104 as will be further described below. The transmitter 104 may include a transmit coil 114 for outputting an energy transmission. The receiver 108 further includes a receive coil 118 for receiving or capturing energy from the energy transmission. The near-field may correspond to a region in which there are strong reactive fields resulting from the currents and charges in the transmit coil 114 that minimally radiate power away from the transmit coil 114. In some cases the near-field may correspond to a region that is within about one wavelength (or a fraction thereof) of the transmit coil 114. The transmit and receive coils 114 and 118 are sized according to applications and devices to be associated therewith. As described above, efficient energy transfer may occur by coupling a large portion of the energy in a field 105 of the transmit coil 114 to a receive coil 118 rather than propagating most of the energy in an electromagnetic wave to the far field. When positioned within the field 105, a “coupling mode” may be developed between the transmit coil 114 and the receive coil 118. The area around the

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transmit and receive coils 114 and 118 where this coupling may occur is referred to herein as a coupling-mode region.

FIG. 2 is a functional block diagram of exemplary components that may be used in the wireless power transfer system 100 of FIG. 1, in accordance with various exemplary embodiments. The transmitter 204 may include transmit circuitry 206 that may include an oscillator 222, a driver circuit 224, and a filter and matching circuit 226. The oscillator 222 may be configured to generate a signal at a desired frequency, such as 468.75 KHz, 6.78 MHz or 13.56 MHz, that may be adjusted in response to a frequency control signal 223. The oscillator signal may be provided to a driver circuit 224 configured to drive the transmit coil 214 at, for example, a resonant frequency of the transmit coil 214. The driver circuit 224 may be a switching amplifier configured to receive a square wave from the oscillator 222 and output a sine wave. For example, the driver circuit 224 may be a class E amplifier. A filter and matching circuit 226 may be also included to filter out harmonics or other unwanted frequencies and match the impedance of the transmitter 204 to the transmit coil 214.

The receiver 208 may include receive circuitry 210 that may include a matching circuit 232 and a rectifier and switching circuit 234 to generate a DC power output from an AC power input to charge a battery 236 as shown in FIG. 2 or to power a device (not shown) coupled to the receiver 108. The matching circuit 232 may be included to match the impedance of the receive circuitry 210 to the receive coil 218. The receiver 208 and transmitter 204 may additionally communicate on a separate communication channel 219 (e.g., Bluetooth, zigbee, cellular, etc). The receiver 208 and transmitter 204 may alternatively communicate via in-band signaling using characteristics of the wireless field 206.

As described more fully below, receiver 208, that may initially have a selectively disablable associated load (e.g., battery 236), may be configured to determine whether an amount of power transmitted by transmitter 204 and receiver by receiver 208 is appropriate for charging a battery 236. Further, receiver 208 may be configured to enable a load (e.g., battery 236) upon determining that the amount of power is appropriate. In some embodiments, a receiver 208 may be configured to directly utilize power received from a wireless power transfer field without charging of a battery 236. For example, a communication device, such as a near-field communication (NFC) or radio-frequency identification device (RFID) may be configured to receive power from a wireless power transfer field and communicate by interacting with the wireless power transfer field and/or utilize the received power to communicate with a transmitter 204 or other devices.

FIG. 3 is a schematic diagram of a portion of transmit circuitry 206 or receive circuitry 210 of FIG. 2 including a transmit or receive coil 352, in accordance with exemplary embodiments. As illustrated in FIG. 3, transmit or receive circuitry 350 used in exemplary embodiments may include a coil 352. The coil may also be referred to or be configured as a “loop” antenna 352. The coil 352 may also be referred to herein or be configured as a “magnetic” antenna or an induction coil. The term “coil” is intended to refer to a component that may wirelessly output or receive energy for coupling to another “coil.” The coil may also be referred to as an “antenna” of a type that is configured to wirelessly output or receive power. The coil 352 may be configured to include an air core or a physical core such as a ferrite core (not shown). Air core loop coils may be more tolerable to extraneous physical devices placed in the vicinity of the core. Furthermore, an air core loop coil 352 allows the placement of other components within the core area. In addition, an air core loop may more readily enable placement of the receive coil 218

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(FIG. 2) within a plane of the transmit coil **214** (FIG. 2) where the coupled-mode region of the transmit coil **214** (FIG. 2) may be more powerful.

As stated, efficient transfer of energy between the transmitter **104** and receiver **108** may occur during matched or nearly matched resonance between the transmitter **104** and the receiver **108**. However, even when resonance between the transmitter **104** and receiver **108** are not matched, energy may be transferred, although the efficiency may be affected. Transfer of energy occurs by coupling energy from the field **105** of the transmitting coil to the receiving coil residing in the neighborhood where this field **105** is established rather than propagating the energy from the transmitting coil into free space.

The resonant frequency of the loop or magnetic coils is based on the inductance and capacitance. Inductance may be simply the inductance created by the coil **352**, whereas, capacitance may be added to the coil's inductance to create a resonant structure at a desired resonant frequency. As a non-limiting example, capacitor **352** and capacitor **354** may be added to the transmit or receive circuitry **350** to create a resonant circuit that selects a signal **356** at a resonant frequency. Accordingly, for larger diameter coils, the size of capacitance needed to sustain resonance may decrease as the diameter or inductance of the loop increases. Furthermore, as the diameter of the coil increases, the efficient energy transfer area of the near-field may increase. Other resonant circuits formed using other components are also possible. As another non-limiting example, a capacitor may be placed in parallel between the two terminals of the coil **350**. For transmit coils, a signal **358** with a frequency that substantially corresponds to the resonant frequency of the coil **352** may be an input to the coil **352**.

In one embodiment, the transmitter **104** may be configured to output a time varying magnetic field with a frequency corresponding to the resonant frequency of the transmit coil **114**. When the receiver is within the field **105**, the time varying magnetic field may induce a current in the receive coil **118**. As described above, if the receive coil **118** is configured to be resonant at the frequency of the transmit coil **118**, energy may be efficiently transferred. The AC signal induced in the receive coil **118** may be rectified as described above to produce a DC signal that may be provided to charge or to power a load.

FIG. 4 is a functional block diagram of a transmitter **404** that may be used in the wireless power transfer system of FIG. 1, in accordance with exemplary embodiments. The transmitter **404** may include transmit circuitry **406** and a transmit coil **414**. The transmit coil **414** may be the coil **352** as shown in FIG. 3. Transmit circuitry **406** may provide RF power to the transmit coil **414** by providing an oscillating signal resulting in generation of energy (e.g., magnetic flux) about the transmit coil **414**. Transmitter **404** may operate at any suitable frequency. By way of example, transmitter **404** may operate at the 13.56 MHz ISM band.

Transmit circuitry **406** may include a fixed impedance matching circuit **409** for matching the impedance of the transmit circuitry **406** (e.g., 50 ohms) to the transmit coil **414** and a low pass filter (LPF) **408** configured to reduce harmonic emissions to levels to prevent self-jamming of devices coupled to receivers **108** (FIG. 1). Other exemplary embodiments may include different filter topologies, including but not limited to, notch filters that attenuate specific frequencies while passing others and may include an adaptive impedance match, that may be varied based on measurable transmit metrics, such as output power to the coil **414** or DC current drawn by the driver circuit **424**. Transmit circuitry **406** further

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includes a driver circuit **424** configured to drive an RF signal as determined by an oscillator **423**. The transmit circuitry **406** may be comprised of discrete devices or circuits, or alternatively, may be comprised of an integrated assembly. An exemplary RF power output from transmit coil **414** may be on the order of 2.5 Watts.

Transmit circuitry **406** may further include a controller **415** for selectively enabling the oscillator **423** during transmit phases (or duty cycles) for specific receivers, for adjusting the frequency or phase of the oscillator **423**, and for adjusting the output power level for implementing a communication protocol for interacting with neighboring devices through their attached receivers. It is noted that the controller **415** may also be referred to herein as processor **415**. Adjustment of oscillator phase and related circuitry in the transmission path may allow for reduction of out of band emissions, especially when transitioning from one frequency to another.

The transmit circuitry **406** may further include a load sensing circuit **416** for detecting the presence or absence of active receivers in the vicinity of the near-field generated by transmit coil **414**. By way of example, a load sensing circuit **416** monitors the current flowing to the driver circuit **424**, that may be affected by the presence or absence of active receivers in the vicinity of the field generated by transmit coil **414** as will be further described below. Detection of changes to the loading on the driver circuit **424** are monitored by controller **415** for use in determining whether to enable the oscillator **423** for transmitting energy and to communicate with an active receiver. As described more fully below, a current measured at the driver circuit **424** may be used to determine whether an invalid device is positioned within a wireless power transfer region of the transmitter **404**.

The transmit coil **414** may be implemented with a Litz wire or as an antenna strip with the thickness, width and metal type selected to keep resistive losses low. In a one implementation, the transmit coil **414** may generally be configured for association with a larger structure such as a table, mat, lamp or other less portable configuration. Accordingly, the transmit coil **414** generally may not need "turns" in order to be of a practical dimension. An exemplary implementation of a transmit coil **414** may be "electrically small" (i.e., fraction of the wavelength) and tuned to resonate at lower usable frequencies by using capacitors to define the resonant frequency.

The transmitter **404** may gather and track information about the whereabouts and status of receiver devices that may be associated with the transmitter **404**. Thus, the transmit circuitry **406** may include a presence detector **480**, an enclosed detector **460**, or a combination thereof, connected to the controller **415** (also referred to as a processor herein). The controller **415** may adjust an amount of power delivered by the driver circuit **424** in response to presence signals from the presence detector **480** and the enclosed detector **460**. The transmitter **404** may receive power through a number of power sources, such as, for example, an AC-DC converter (not shown) to convert conventional AC power present in a building, a DC-DC converter (not shown) to convert a conventional DC power source to a voltage suitable for the transmitter **404**, or directly from a conventional DC power source (not shown).

As a non-limiting example, the presence detector **480** may be a motion detector utilized to sense the initial presence of a device to be charged that is inserted into the coverage area of the transmitter **404**. After detection, the transmitter **404** may be turned on and the RF power received by the device may be used to toggle a switch on the Rx device in a pre-determined manner, which in turn results in changes to the driving point impedance of the transmitter **404**.

As another non-limiting example, the presence detector **480** may be a detector capable of detecting a human, for example, by infrared detection, motion detection, or other suitable means. In some exemplary embodiments, there may be regulations limiting the amount of power that a transmit coil **414** may transmit at a specific frequency. In some cases, these regulations are meant to protect humans from electromagnetic radiation. However, there may be environments where a transmit coil **414** is placed in areas not occupied by humans, or occupied infrequently by humans, such as, for example, garages, factory floors, shops, and the like. If these environments are free from humans, it may be permissible to increase the power output of the transmit coil **414** above the normal power restrictions regulations. In other words, the controller **415** may adjust the power output of the transmit coil **414** to a regulatory level or lower in response to human presence and adjust the power output of the transmit coil **414** to a level above the regulatory level when a human is outside a regulatory distance from the electromagnetic field of the transmit coil **414**.

As a non-limiting example, the enclosed detector **460** (may also be referred to herein as an enclosed compartment detector or an enclosed space detector) may be a device such as a sense switch for determining when an enclosure is in a closed or open state. When a transmitter is in an enclosure that is in an enclosed state, a power level of the transmitter may be increased.

In exemplary embodiments, a method by which the transmitter **404** does not remain on indefinitely may be used. In this case, the transmitter **404** may be programmed to shut off after a user-determined amount of time. This feature prevents the transmitter **404**, notably the driver circuit **424**, from running long after the wireless devices in its perimeter are fully charged. This event may be due to the failure of the circuit to detect the signal sent from either the repeater or the receive coil that a device is fully charged. To prevent the transmitter **404** from automatically shutting down if another device is placed in its perimeter, the transmitter **404** automatic shut off feature may be activated only after a set period of lack of motion detected in its perimeter. The user may be able to determine the inactivity time interval, and change it as desired. As a non-limiting example, the time interval may be longer than that needed to fully charge a specific type of wireless device under the assumption of the device being initially fully discharged.

FIG. 5 is a functional block diagram of a receiver **508** that may be used in the wireless power transfer system of FIG. 1, in accordance with exemplary embodiments. The receiver **508** includes receive circuitry **510** that may include a receive coil **518**. Receiver **508** further couples to device **550** for providing received power thereto. It should be noted that receiver **508** is illustrated as being external to device **550** but may be integrated into device **550**. Energy may be propagated wirelessly to receive coil **518** and then coupled through the rest of the receive circuitry **510** to device **550**. By way of example, the charging device may include devices such as mobile phones, portable music players, laptop computers, tablet computers, computer peripheral devices, communication devices (e.g., Bluetooth devices), digital cameras, hearing aids (an other medical devices), and the like.

Receive coil **518** may be tuned to resonate at the same frequency, or within a specified range of frequencies, as transmit coil **414** (FIG. 4). Receive coil **518** may be similarly dimensioned with transmit coil **414** or may be differently sized based upon the dimensions of the associated device **550**. By way of example, device **550** may be a portable electronic device having diametric or length dimension smaller than the

diameter or length of transmit coil **414**. In such an example, receive coil **518** may be implemented as a multi-turn coil in order to reduce the capacitance value of a tuning capacitor (not shown) and increase the receive coil's impedance. By way of example, receive coil **518** may be placed around the substantial circumference of device **550** in order to maximize the coil diameter and reduce the number of loop turns (i.e., windings) of the receive coil **518** and the inter-winding capacitance.

Receive circuitry **510** may provide an impedance match to the receive coil **518**. Receive circuitry **510** includes power conversion circuitry **506** for converting a received RF energy source into charging power for use by the device **550**. Power conversion circuitry **506** includes an RF-to-DC converter **520** and may also include a DC-to-DC converter **522**. RF-to-DC converter **520** rectifies the RF energy signal received at receive coil **518** into a non-alternating power with an output voltage represented by  $V_{rect}$ . The DC-to-DC converter **522** (or other power regulator) converts the rectified RF energy signal into an energy potential (e.g., voltage) that is compatible with device **550** with an output voltage and output current represented by  $V_{out}$  and  $I_{out}$ . Various RF-to-DC converters are contemplated, including partial and full rectifiers, regulators, bridges, doublers, as well as linear and switching converters.

Receive circuitry **510** may further include switching circuitry **512** for connecting receive coil **518** to the power conversion circuitry **506** or alternatively for disconnecting the power conversion circuitry **506**. Disconnecting receive coil **518** from power conversion circuitry **506** not only suspends charging of device **550**, but also changes the "load" as "seen" by the transmitter **404** (FIG. 2).

As disclosed above, transmitter **404** includes load sensing circuit **416** that may detect fluctuations in the bias current provided to transmitter driver circuit **424**. Accordingly, transmitter **404** has a mechanism for determining when receivers are present in the transmitter's near-field.

When multiple receivers **508** are present in a transmitter's near-field, it may be desirable to time-multiplex the loading and unloading of one or more receivers to enable other receivers to more efficiently couple to the transmitter. A receiver **508** may also be cloaked in order to eliminate coupling to other nearby receivers or to reduce loading on nearby transmitters. This "unloading" of a receiver is also known herein as a "cloaking." Furthermore, this switching between unloading and loading controlled by receiver **508** and detected by transmitter **404** may provide a communication mechanism from receiver **508** to transmitter **404** as is explained more fully below. Additionally, a protocol may be associated with the switching that enables the sending of a message from receiver **508** to transmitter **404**. By way of example, a switching speed may be on the order of 100  $\mu\text{sec}$ .

In an exemplary embodiment, communication between the transmitter **404** and the receiver **508** refers to a device sensing and charging control mechanism, rather than conventional two-way communication (i.e., in band signaling using the coupling field). In other words, the transmitter **404** may use on/off keying of the transmitted signal to adjust whether energy is available in the near-field. The receiver may interpret these changes in energy as a message from the transmitter **404**. From the receiver side, the receiver **508** may use tuning and de-tuning of the receive coil **518** to adjust how much power is being accepted from the field. In some cases, the tuning and de-tuning may be accomplished via the switching circuitry **512**. The transmitter **404** may detect this difference in power used from the field and interpret these changes as a

message from the receiver 508. It is noted that other forms of modulation of the transmit power and the load behavior may be utilized.

Receive circuitry 510 may further include signaling detector and beacon circuitry 514 used to identify received energy fluctuations, that may correspond to informational signaling from the transmitter to the receiver. Furthermore, signaling and beacon circuitry 514 may also be used to detect the transmission of a reduced RF signal energy (i.e., a beacon signal) and to rectify the reduced RF signal energy into a nominal power for awakening either un-powered or power-depleted circuits within receive circuitry 510 in order to configure receive circuitry 510 for wireless charging.

Receive circuitry 510 further includes processor 516 for coordinating the processes of receiver 508 described herein including the control of switching circuitry 512 described herein. Cloaking of receiver 508 may also occur upon the occurrence of other events including detection of an external wired charging source (e.g., wall/USB power) providing charging power to device 550. Processor 516, in addition to controlling the cloaking of the receiver, may also monitor beacon circuitry 514 to determine a beacon state and extract messages sent from the transmitter 404. Processor 516 may also adjust the DC-to-DC converter 522 for improved performance.

FIG. 6 is a schematic diagram of a portion of transmit circuitry 600 that may be used in the transmit circuitry 406 of FIG. 4. The transmit circuitry 600 may include a driver circuit 624 as described above in FIG. 4. As described above, the driver circuit 624 may be a switching amplifier that may be configured to receive a square wave and output a sine wave to be provided to the transmit circuit 650. In some cases the driver circuit 624 may be referred to as an amplifier circuit. The driver circuit 624 is shown as a class E amplifier, however, any suitable driver circuit 624 may be used in accordance with embodiments. For example, another driver circuit, driver circuit 724, may be used in accordance with embodiments disclosed herein as is described and illustrated with respect to FIGS. 7-9. The driver circuit 624 may be driven by an input signal 602 from an oscillator 423 as shown in FIG. 4. The driver circuit 624 may also be provided with a drive voltage  $V_D$  that is configured to control the maximum power that may be delivered through a transmit circuit 650. To eliminate or reduce harmonics and/or to reduce electromagnetic interference (EMI), the transmit circuitry 600 may include a filter circuit 626. The filter circuit 626 may be a three pole (capacitor 634, inductor 632, and capacitor 636) low pass filter circuit 626. In other embodiments, not shown, the filter circuit 626 may be designed in a different configuration, such as the filter circuit, filter circuit 726, described herein with respect to FIGS. 7-8.

The signal output by the filter circuit 626 may be provided to a transmit circuit 650 comprising a coil 614. The transmit circuit 650 may include a series resonant circuit having a capacitance 620 and inductance (e.g., that may be due to the inductance or capacitance of the coil or to an additional capacitor component) that may resonate at a frequency of the filtered signal provided by the driver circuit 624. The load of the transmit circuit 650 may be represented by the variable resistor 622. The load may be a function of a wireless power receiver 508 that is positioned to receive power from the transmit circuit 650.

In some aspects, a driver, such as a push-pull driver, may be used to increase output power for a given level of component stress and reduce EMI. In addition, filtering circuits may be used to further reduce EMI in order to, for example, meet regulatory requirements pertaining to EMI. However, the

components needed to reduce EMI may increase the size of a transmitter, increase the cost of producing the transmitter, and lead to efficiency losses. Other driver designs, such as a single transistor differential driver, may reduce EMI without substantial increases in the size of the transmitter or the cost of producing the transmitter. However, a single transistor differential driver may not be able to increase output power for a given level of component stress to the extent of a push-pull driver. As described herein with respect to FIGS. 7-11, a driver with an inverted stage may be able to increase output power for a given level of component stress to the same or similar extent as a push-pull driver while also reducing the size of the transmitter, reducing the cost of producing the transmitter, reducing efficiency losses, and/or reducing EMI to levels that are comparable to a single transistor differential driver.

FIG. 7 is a functional block of a portion of a wireless power transmitter 700 that may be used in the wireless power transfer system of FIG. 1. The transmitter 700 may include the driver circuit 724, a filter circuit 726, and a load 750. In some aspects, the driver circuit 724 may replace, for example, driver circuit 624 of FIG. 6, the filter circuit 726 may replace, for example, filter circuit 626 of FIG. 6, and the load 750 may replace, for example, load 650 of FIG. 6. Note that in other aspects, the driver circuit 724, filter circuit 726, and/or load 750 need not be limited to wireless power transmitters, and may be employed in any suitable circuit or device.

Driver circuit 724 may include a first driver stage 702 and a second driver stage 704. In some embodiments, the first driver stage 702 may be non-inverted and the second driver stage 704 may be inverted. In other embodiments, the first driver stage 702 may be inverted and the second driver stage 704 may be non-inverted. First driver stage 702 and second driver stage 704 may be driven by one or more input signals from an oscillator 423 as shown in FIG. 4.

Filter circuit 726 may include first filter 706 and/or second filter 708. Note that while two separate filters 706 and 708 are depicted in FIG. 7, this is not meant to be limiting and filter circuit 726 may include any number of filters. In some embodiments, a signal produced by the first driver stage 702 may pass through first filter 706 and a signal produced by the second driver stage 704 may pass through second filter 708. Once filtered, the signals may pass to a load 750. Note that, as described herein, the filter circuit 726 may be optional. In some embodiments, the signal produced by the first driver stage 702 and the second driver stage 704 may directly drive the load 750. For example, the filter circuit 726 may be optional if the driver circuit 724 reduces EMI sufficiently so as to meet regulatory requirements.

FIGS. 8A-B illustrate a schematic diagram 800 of a driver circuit 724 and a filter circuit 726 that may be used in the wireless power transmitter 700 of FIG. 7. The driver circuit 724 may include a transistor 806, an inductor 810a, and capacitors 812 and 816, which together may comprise the first driver stage 702 of FIG. 7. The driver circuit 724 may further include a transistor 808, an inductor 810b, and capacitors 814 and 818, which together may comprise the second driver stage 704 of FIG. 7.

Transistor 806 may be driven by input signal 802 from an oscillator 423 as shown in FIG. 4. As an example, transistor 806 may be an NMOS transistor, a PMOS transistor, or any other type of transistor. Inductor 810a may be coupled to the drain of the transistor 806 at node 870 and may be considered a choke inductor. The output at node 870 may be a non-inverted signal, as is described in more detail herein with respect to FIG. 9.

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Likewise, transistor **808** may be driven by input signal **804** from an oscillator **423** as shown in FIG. **4**. In some embodiments, the input signal **802** and the input signal **804** may be of the same phase such that transistors **806** and **808** are driven in phase. As an example, transistor **808** may be an NMOS transistor, a PMOS transistor, or any other type of transistor. Inductor **810b** may be coupled to the source of the transistor **808** at node **880** and may be considered a choke inductor. The output at node **880** may be an inverted signal, as is described in more detail herein with respect to FIG. **9**.

In some embodiments, as illustrated in FIG. **8A**, the second driver stage **704** may be considered an inverted stage because a high side of the transistor **808** (e.g., the drain side) is connected directly to a source (e.g., a positive voltage supply) and a load side of the transistor **808** (e.g., the source side) is coupled to the inductor **810b**. This is opposite from the configuration of the first driver stage **702** (e.g., transistor **806** and inductor **810a**). In other embodiments, as illustrated in FIG. **8B**, the second driver stage **704** may be considered an inverted stage because a high side of the transistor **808** (e.g., the drain side) is connected to the inductor **810b**, the inductor **810b** is coupled to  $-VCC$  (e.g., a negative voltage supply), and a load side of the transistor **808** (e.g., the source side) is connected directly to ground. This is similar to the configuration of the first driver stage **702** (e.g., transistor **806** and inductor **810a**), except that the inductor **810a** may be coupled to  $+VCC$  (e.g., a positive voltage supply).

Because the transistors **806** and **808** are driven in phase, and because transistor **808** is configured in an inverted fashion as compared to transistor **806**, the outputs produced at node **870** and node **880** are mirror images of each other. The signals are described in more detail with respect to FIG. **9**. In some embodiments, the mirroring of the outputs may reduce common mode noise. In this way, the driver circuit **724**, due to the reduction in common mode noise, may reduce the need for additional filtering in the transmitter **700** or other device to reduce EMI. Because of the need for fewer components, this may result in a reduction in size and/or cost of the transmitter **700** or other device. This may also result in efficiency gains, as a need for fewer components results in a need for less copper, and copper may introduce resistive loss.

The filter circuit **726** may include inductors **820a** and **826a** and capacitors **822** and **828**, which together may comprise the first filter **706**. The filter circuit **726** may further include inductors **820b** and **826b** and capacitors **824** and **830**, which together may comprise the second filter **708**. Note that while certain components are depicted in the filter circuit **726**, this is not meant to be limiting and any number of components may be included in the filter circuit **726**. Furthermore, in some embodiments, the filter circuit **726** may be optional such that capacitors **816** and/or **818** are coupled to the load **750**. As described herein, the filter circuit **726** may function to filter harmonics and/or reduce EMI. Note that while the load **750** comprises two resistors **832** and **834**, this is not meant to be limiting and the load **750** may comprise any circuit that is to be driven by a driver.

As described herein, the topology of the driver circuit **724** may allow for a reduction in the size and/or cost of the transmitter **700** or other device. Furthermore, in some embodiments, inductor **810a** may be inductively coupled to inductor **810b**, inductor **820a** may be inductively coupled to inductor **820b**, and/or inductor **826a** may be inductively coupled to inductor **826b** to allow for further reductions in the size and/or cost of the transmitter **700** or other device. For example, two or more inductors may be inductively coupled by interleaving their core material. As another example, two or more inductors may be inductively coupled by winding them around the same core material.

In some embodiments, a current that passes through inductor **810a** may flow in an opposite direction as a current that

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passes through inductor **810b**. Likewise, a current that passes through inductor **820a** may flow in an opposite direction as a current that passes through inductor **820b**, and a current that passes through inductor **826a** may flow in an opposite direction as a current that passes through inductor **826b**. Because the current flow may be opposite, the three pairs of inductors may be inductively coupled without creating noise or other undesirable signals. Generally, inductors may consume a large amount of power in a transmitter or other device, and large inductors may be needed to filter harmonics and/or reduce EMI. However, by making use of mutual inductance, the effective size of the inductors, such as inductors **810a** and **810b**, may be doubled without a change in the actual size of the inductors. In this way, a size of inductors **810a**, **810b**, **820a**, **820b**, **826a**, and/or **826b** may be reduced by a factor of two to achieve the same level of EMI reduction. Inductive coupling may also reduce an imbalance between stages that is often seen in traditional push-pull drivers, and efficiency may be improved because a series resistance of the driver circuit **724** and the filter circuit **726** may be reduced by up to a factor of two. Note that traditional push-pull drivers may not be able to inductively couple their inductors because the drivers may be driven out of phase (e.g., 180 degrees out of phase) and/or current may flow in the same direction, which would result in noise and/or other undesirable signals.

FIG. **9** is a waveform diagram **900** that may be generated by the driver circuit **724** of FIGS. **7-8**. Waveform **902** may represent the output of transistor **806** at node **870** and waveform **904** may represent the output of transistor **808** at node **880**. As described herein, waveforms **902** and **904** are mirror images of each other. While FIG. **9** depicts a peak of waveform **902** reaching approximately 60V, this is not meant to be limiting as the driver circuit **724** may be designed such that the output produced reaches any voltage level. Likewise, while FIG. **9** depicts a trough of waveform **904** reaching approximately -40V, this is not meant to be limiting as the driver circuit **724** may be designed such that the output produced reaches any voltage level.

In some embodiments, the waveform **902** reaches a peak at the same or at nearly the same time as the waveform **904** reaches a trough. Thus, the two waveforms **902** and **904** are in phase or are nearly in phase, which may result in benefits as described herein. As an example, and not meant to be limiting, a frequency of the waveforms **902** and **904** may be 6.67 MHz.

FIG. **10** is a flowchart of an exemplary method **1000** for amplifying a voltage in a wireless power transmitter. Although the method of flowchart **1000** is described herein with reference to the transmitter **700** discussed above with respect to FIGS. **7-9**, a person having ordinary skill in the art will appreciate that the method of flowchart **1000** may be implemented by the transmitter **104** discussed above with respect to FIG. **1**, the transmitter **204** discussed above with respect to FIG. **2**, and/or any other suitable device (e.g., not necessarily a device that operates in a wireless power system). In an embodiment, the steps in flowchart **1000** may be performed by a driver in conjunction with one or more of the driver circuit **724**, the first driver stage **702**, and the second driver stage **704**. Although the method of flowchart **1000** is described herein with reference to a particular order, in various embodiments, blocks herein may be performed in a different order, or omitted, and additional blocks may be added. A person having ordinary skill in the art will appreciate that the method of flowchart **1000** may be implemented in any communication device that may be configured to transmit power to a wireless power receiver and communicate with the wireless power receiver.

At block **1002**, the method **1000** generates, by a first circuit, a first signal, where the first signal comprises a non-inverted output. For example, an output produced by a tran-

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sistor, like transistor **806** of FIGS. **8A-B**, may be non-inverted as compared to an output produced by a transistor, like transistor **808** of FIGS. **8A-B**, of an inverted stage. The first signal may be similar to waveform **902** of FIG. **9**. At block **1004**, the method **1000** generates, by a second circuit, a second signal in phase with the first signal, where the second signal comprises an inverted output. For example, the second signal may be similar to waveform **904** of FIG. **9**. At block **1006**, the method **1000** drives a load based on the first signal and the second signal.

FIG. **11** is a functional block diagram of a wireless power transmitter **1100**, in accordance with an exemplary embodiment. Wireless power transmitter **1100** comprises means **1102**, means **1104**, and means **1106** for the various actions discussed with respect to FIGS. **1-10**. The transmitter **1100** includes means **1102** for generating a first signal, where the first signal comprises a non-inverted output. In an embodiment, means **1102** for generating a first signal, where the first signal comprises a non-inverted output, may be configured to perform one or more of the functions discussed above with respect to block **1002**. The transmitter **1100** further includes means **1104** for generating a second signal in phase with the first signal, where the second signal comprises an inverted output. In an embodiment, means **1104** for generating a second signal in phase with the first signal, where the second signal comprises an inverted output, may be configured to perform one or more of the functions discussed above with respect to block **1004**. The transmitter **1100** further includes means **1106** for driving a load based on the first signal and the second signal. In an embodiment, means **1106** for driving a load based on the first signal and the second signal may be configured to perform one or more of the functions discussed above with respect to block **1006**.

The various operations of methods described above may be performed by any suitable means capable of performing the operations, such as various hardware and/or software component(s), circuits, and/or module(s). Generally, any operations illustrated in the Figures may be performed by corresponding functional means capable of performing the operations. Means for generating a first signal may be provided by a driver. Means for generating a second signal may be provided by a driver. Means for driving a load based on a first signal and a second signal may be provided by a driver.

Information and signals may be represented using any of a variety of different technologies and techniques. For example, data, instructions, commands, information, signals, bits, symbols, and chips that may be referenced throughout the above description may be represented by voltages, currents, electromagnetic waves, magnetic fields or particles, optical fields or particles, or any combination thereof.

The various illustrative logical blocks, modules, circuits, and algorithm steps described in connection with the embodiments disclosed herein may be implemented as electronic hardware, computer software, or combinations of both. To clearly illustrate this interchangeability of hardware and software, various illustrative components, blocks, modules, circuits, and steps have been described above generally in terms of their functionality. Whether such functionality is implemented as hardware or software depends upon the particular application and design constraints imposed on the overall system. The described functionality may be implemented in varying ways for each particular application, but such implementation decisions should not be interpreted as causing a departure from the scope of the embodiments of the invention.

The various illustrative blocks, modules, and circuits described in connection with the embodiments disclosed herein may be implemented or performed with a general purpose processor, a Digital Signal Processor (DSP), an Application Specific Integrated Circuit (ASIC), a Field Pro-

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grammable Gate Array (FPGA) or other programmable logic device, discrete gate or transistor logic, discrete hardware components, or any combination thereof designed to perform the functions described herein. A general purpose processor may be a microprocessor, but in the alternative, the processor may be any conventional processor, controller, microcontroller, or state machine. A processor may also be implemented as a combination of computing devices, e.g., a combination of a DSP and a microprocessor, a plurality of microprocessors, one or more microprocessors in conjunction with a DSP core, or any other such configuration.

The steps of a method or algorithm and functions described in connection with the embodiments disclosed herein may be embodied directly in hardware, in a software module executed by a processor, or in a combination of the two. If implemented in software, the functions may be stored on or transmitted over as one or more instructions or code on a tangible, non-transitory computer-readable medium. A software module may reside in Random Access Memory (RAM), flash memory, Read Only Memory (ROM), Electrically Programmable ROM (EPROM), Electrically Erasable Programmable ROM (EEPROM), registers, hard disk, a removable disk, a CD ROM, or any other form of storage medium known in the art. A storage medium is coupled to the processor such that the processor can read information from, and write information to, the storage medium. In the alternative, the storage medium may be integral to the processor. Disk and disc, as used herein, includes compact disc (CD), laser disc, optical disc, digital versatile disc (DVD), floppy disk and blu ray disc where disks usually reproduce data magnetically, while discs reproduce data optically with lasers. Combinations of the above should also be included within the scope of computer readable media. The processor and the storage medium may reside in an ASIC. The ASIC may reside in a user terminal. In the alternative, the processor and the storage medium may reside as discrete components in a user terminal.

For purposes of summarizing the disclosure, certain aspects, advantages and novel features of the inventions have been described herein. It is to be understood that not necessarily all such advantages may be achieved in accordance with any particular embodiment of the invention. Thus, the invention may be embodied or carried out in a manner that achieves or optimizes one advantage or group of advantages as taught herein without necessarily achieving other advantages as may be taught or suggested herein.

Various modifications of the above described embodiments will be readily apparent, and the generic principles defined herein may be applied to other embodiments without departing from the spirit or scope of the invention. Thus, the present invention is not intended to be limited to the embodiments shown herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein.

What is claimed is:

1. A transmitter for transferring power wirelessly, comprising:

a first circuit configured to output a first signal, the first signal comprising a non-inverted output, the first circuit comprising a first choke inductor; and  
a second circuit configured to output a second signal in phase with the first signal, the second signal comprising an inverted output with respect to the non-inverted output such that the second signal is a mirror of the first signal, the second circuit comprising a second choke inductor inductively coupled to the first choke inductor, the first circuit and the second circuit further configured to drive a load.

2. The transmitter of claim 1, wherein the first circuit comprises a first driver and a first filter and the second circuit comprises a second driver and a second filter.



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3. The transmitter of claim 2, wherein the first driver comprises a first switch and the first choke inductor and the second driver comprises a second switch and the second choke inductor.

4. The transmitter of claim 3, wherein the first choke inductor is coupled to a drain of the first switch, and wherein the second choke inductor is coupled to a source of the second switch.

5. The transmitter of claim 3, wherein the first choke inductor is coupled to a node of the first switch and a positive supply, and wherein the second choke inductor is coupled to a node of the second switch and a negative supply.

6. The transmitter of claim 5, wherein the first switch comprises a first transistor and the second switch comprises a second transistor, wherein the first choke inductor is coupled to a drain of the first transistor and the positive supply, and wherein the second choke inductor is coupled to a drain of the second transistor and the negative supply.

7. The transmitter of claim 2, wherein the first filter comprises a third inductor and the second filter comprises a fourth inductor, and wherein the third inductor is inductively coupled to the fourth inductor.

8. The transmitter of claim 7, wherein the first filter further comprises a fifth inductor and the second filter further comprises a sixth inductor, and wherein the fifth inductor is inductively coupled to the sixth inductor.

9. The transmitter of claim 1, wherein the first circuit is further configured to supply substantially half of a current to the load, and wherein the second circuit is further configured to supply substantially half of the current to the load.

10. The transmitter of claim 1, wherein the first circuit and the second circuit further are configured to drive a coil configured to generate a wireless field for wireless power transfer.

11. A method for amplifying a voltage, comprising:  
outputting, by a first circuit, a first signal, the first signal comprising a non-inverted output, the first circuit comprising a first choke inductor;  
outputting, by a second circuit, a second signal in phase with the first signal, the second signal comprising an inverted output with respect to the non-inverted output such that the second signal is a mirror of the first signal, the second circuit comprising a second choke inductor inductively coupled to the first choke inductor; and  
driving a load based on the first signal and the second signal.

12. The method of claim 11, wherein the outputting the first signal further comprises:  
coupling the first choke inductor to a ground during a first period of time; and  
coupling the first choke inductor to a supply during a second period of time.

13. The method of claim 12, wherein the outputting the second signal further comprises:  
coupling the second choke inductor to the supply during the first period of time; and  
coupling the second choke inductor to the ground during the second period of time.

14. The method of claim 13, wherein the first choke inductor is of a same inductance as the second choke inductor.

15. The method of claim 11, further comprising filtering the first signal by a first filter and filtering the second signal by a second filter.

16. The method of claim 15, wherein filtering the first signal further comprises filtering the first signal based on a third inductor, and wherein filtering the second signal further comprises filtering the second signal based on a fourth inductor.

17. The method of claim 16, wherein the third inductor is inductively coupled to the fourth inductor.

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18. The method of claim 11, wherein driving a load based on the first signal and the second signal comprises driving a coil configured to generate a wireless field for wireless power transfer based on the first signal and the second signal.

19. An apparatus configured to amplify a voltage, comprising:

means for outputting a first signal, the first signal comprising a non-inverted output, the means for outputting a first signal comprising a first choke inductor;

means for outputting a second signal in phase with the first signal, the second signal comprising an inverted output with respect to the non-inverted output such that the second signal is a mirror of the first signal, the means for outputting a second signal comprising a second choke inductor inductively coupled to the first choke inductor; and

means for driving a load based on the first signal and the second signal.

20. The apparatus of claim 19, wherein the means for outputting the first signal further comprises:

means for coupling the first choke inductor to a ground during a first period of time; and

means for coupling the first choke inductor to a supply during a second period of time.

21. The apparatus of claim 20, wherein the means for outputting the second signal further comprises:

means for coupling the second choke inductor to the supply during the first period of time; and

means for coupling the second choke inductor to the ground during the second period of time.

22. The apparatus of claim 21, wherein the first choke inductor is of a same inductance as the second choke inductor.

23. The apparatus of claim 19, further comprising means for filtering the first signal and means for filtering the second signal.

24. The apparatus of claim 23, wherein means for filtering the first signal further comprises means for filtering the first signal based on a third inductor, and wherein means for filtering the second signal further comprises means for filtering the second signal based on a fourth inductor.

25. The apparatus of claim 24, wherein the third inductor is inductively coupled to the fourth inductor.

26. The apparatus of claim 19, wherein the means for driving a load based on the first signal and the second signal comprises means for driving a coil configured to generate a wireless field for wireless power transfer based on the first signal and the second signal.

27. The apparatus of claim 19, wherein the means for outputting the first signal, the means for outputting the second signal, and the means for driving the load comprise a driver.

28. A non-transitory computer-readable medium comprising code that, when executed, causes an apparatus to:

output, by a first circuit, a first signal, the first signal comprising a non-inverted output, the first circuit comprising a first choke inductor;

output, by a second circuit, a second signal in phase with the first signal, the second signal comprising an inverted output with respect to the non-inverted output such that the second signal is a mirror of the first signal, the second circuit comprising a second choke inductor inductively coupled to the first choke inductor; and

drive a load based on the first signal and the second signal.

29. The medium of claim 28, further comprising code that, when executed, causes the apparatus to:

couple the first choke inductor to a ground during a first period of time; and

couple the first choke inductor to a supply during a second period of time.

30. The medium of claim 29, further comprising code that, when executed, causes the apparatus to:

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couple the second choke inductor to the supply during the first period of time; and  
couple the second choke inductor to the ground during the second period of time.

31. The medium of claim 30, wherein the first choke inductor is of a same inductance as the second choke inductor. 5

32. The medium of claim 28, further comprising code that, when executed, causes the apparatus to filter the first signal by a first filter and filter the second signal by a second filter.

33. The medium of claim 32, further comprising code that, when executed, causes the apparatus to filter the first signal based on a third inductor and filter the second signal based on a fourth inductor. 10

34. The medium of claim 33, wherein the third inductor is inductively coupled to the fourth inductor.

35. The medium of claim 28, further comprising code that, when executed, causes the apparatus to drive a coil configured to generate a wireless field for wireless power transfer based on the first signal and the second signal. 15

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